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VIDEO SWITCHER

BACKGROUND OF THE INVENTION Field of the Invention

The invention relates to a video switching apparatus for switching a plurality of video inputs on a time unit basis of a frame, a field, or the like, outputting them to a recording/reproducing apparatus, and outputting reproduction video images from the recording/reproducing apparatus to a monitor.

Description of the Related Art

Hitherto, a video switching apparatus called a frame switcher or a multiplexer is connected to a recording/reproducing apparatus and used. Upon recording, the video switching apparatus switches a plurality of video signals of monitoring cameras or the like on a time unit basis of a frame, a field, or the like, multiplexes an identification (ID) signal to them, and outputs the resultant video signals, and those signals are recorded by the recording/reproducing apparatus.

Upon reproduction, the ID signal multiplexed to the video signal reproduced from the recording/ reproducing apparatus is read (e.g., by bit reading portion shown herein later) and, when it coincides with a set ID in the video switching apparatus, the video

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signal is written in a memory and outputted to a monitor (e.g., by CPU, video input selecting portion, output means). When it does not coincide with the set ID, the video signal is not written in the memory but the video signal written in the memory is outputted to the monitor.

As mentioned above, when the recorded video images are reproduced while switching a plurality of video images by multiplexing the ID signal to the video signal, the ID signal is set by the video switching apparatus and only the video image to which the same ID signal has been multiplexed can be outputted to the monitor.

In the above conventional video switching apparatus, however, there are two kinds of operating modes of a recording mode and a reproducing mode. The recording/reproducing mode of the video switching apparatus has to be manually set in accordance with the operating mode of the recording/reproducing apparatus connected to the video switching apparatus and there is such a problem that if the operating mode is erroneously set, the video signal cannot be correctly recorded/reproduced.

SUMMARY OF THE INVENTION

The invention intends to solve the above conventional problem and it is an object of the invention to provide an excellent video switching

apparatus which can automatically set a proper operating mode and prevent the erroneous operation.

According to one aspect of the invention, there is provided a video switching apparatus comprising: video input selecting means for switching a plurality of video inputs signals; output means for outputting the switched video input signal to a recording/reproducing apparatus; recording/reproduction detecting means for analyzing a video output of the recording/reproducing apparatus and discriminating whether the recording/reproducing apparatus is in a recording mode or a reproduction mode; and control means for switching its own operation mode to a recording mode or a reproducing mode on the basis of a result determined by the recording/reproduction detecting means.

With this construction, an erroneous operation due to an erroneous setting of the video switching apparatus and the recording/reproducing apparatus can be prevented.

The video switching apparatus of the invention has means for outputting information indicating whether the recording/reproducing apparatus is in the recording mode or reproduction mode to the outside on the basis of the discrimination result of the discriminating means.

With this construction, even from a position where a display or indicator of the recording/ reproducing apparatus cannot be seen, by displaying a

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state of the signal outputted to the outside, the operating mode of the recording/reproducing apparatus can be confirmed.

BRIEF DESCRIPTION OF THE DRAWINGS

The features and advantages of the present invention will become more clearly appreciated from the following description taken in conjunction with the accompanying drawings, in which:

Fig. 1 is a block diagram of a video switching apparatus in the first embodiment of the invention;

Fig. 2 is a flowchart for a recording/ reproduction detecting method in the first embodiment of the invention:

Fig. 3 is a block diagram of a video switching apparatus in the second embodiment of the invention;

Fig. 4 is a flowchart for a recording/
reproduction detecting method in the second embodiment
of the invention; and

Fig. 5 is a diagram of a recording/
20 reproduction detecting circuit in the second embodiment of the invention.

DESCRIPTION OF THE EMBODIMENTS

Fig. 1 is a block diagram of a video switching apparatus in the first embodiment of the invention.

A video switching apparatus 1011 comprises: video input selecting portion 102 for switching a

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plurality of video inputs signals; video signal processing means 103 for processing the selected video signal; output means 104 for outputting the video signal; recording/reproduction detecting means 105 for detecting an operating mode of a recording/reproducing apparatus 118; and an internal sync generating circuit 116.

The video signal processing means 103 comprises: a Y/C separator 106 for separating an input video signal into a luminance signal and color difference signals; a decoder 107 for decoding the video signals outputted from the Y/C separator 106; a memory 108 for storing the video signals; an encoder 109 for encoding the video signals; a bit/ID output portion 110 for outputting an ID signal/a bit signal in which H/L of one or a few bits are outputted at random; and a synthesizing portion 111 for multiplexing or adding the bit signal/ID signal to the video signals from the encoder 109.

The output means 104 comprises: an output circuit 112 for amplifying the video signal to a proper level and outputting the amplified signal; and a selector 113 for selecting an output destination of the video signal.

The recording/reproduction detecting means 105 comprises: a bit reading portion 114 for reading the bit signal multiplexed to the reproduction signal from the recording/reproducing apparatus 118; and a CPU 115 for

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controlling the video input selecting portion 102 and selector 113 and setting the bit signal.

A method of detecting the recording/
reproduction of the first embodiment of the invention
will now be described.

Video output signal in the recording mode and the reproducing mode of the recording/reproducing apparatus 118 will be first described. Usually, the apparatus 118 outputs from its video output terminal the inputted video signal as it is in the recording mode. In the reproducing mode, the video signal recorded on a tape or the like is outputted. That is, in the recording mode, since the video signal outputted from the video switching apparatus 1011 passes through the apparatus 118 and is inputted as it is to the video switching apparatus 1011, the same bit signal as the bit signal set by the CPU 115 can be detected. In the reproducing mode, since the tape which was recorded in the past is reproduced, a bit signal different from the bit signal set by the CPU 115 is detected.

The above recording/reproduction detecting method will now be described with reference to a flowchart of Fig. 2.

The bit signal is detected from the video

output signal of the apparatus 118 by the bit reading

portion 114 (step 1). When the bit signal is detected,

the detected bit signal is compared with the multiplexed

bit signal (multiplexed by synthesizing portion 111)

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(that is, they are compared by CPU 115) (step 2). When the bit signal cannot be detected or when the detected bit signal and the set bit signal continuously coincide by a length of a few fields, the video switching apparatus is set to the recording mode (by using CPU 115, video input selecting portion 102, etc.) (steps 3 and 5). When the detected bit signal and the set bit signal do not coincide, the video switching apparatus 1011 is set to the reproducing mode (steps 3 and 4).

The above method can be also realized by either software or hardware.

When a video transmission cable 119 is not connected from the video output terminal of the apparatus 118 to the input side of the video switching apparatus 1011, since the bit signal cannot be detected by the bit reading portion 114, the video switching apparatus is set to the recording mode.

According to the video switching apparatus in the first embodiment of the invention as mentioned above, the multiplexed bit signal of the video output signal of the recording/reproducing apparatus is detected and compared with the set bit signal, thereby enabling the recording/reproducing mode to be automatically switched without using another control line. Even if the video transmission cable is not connected from the apparatus 118 to the video switching apparatus 1011, since the video switching apparatus is in the recording mode, a mistake such that the video

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signal cannot be recorded can be prevented.

The second embodiment of the invention will be described with reference to Fig. 3.

Fig. 3 is a block diagram of a video switching apparatus in the second embodiment of the invention.

The apparatus of Fig. 3 differs from that of Fig. 1 with respect to a point that recording/ reproduction detecting means 201 comprises: a sync separator 202 for detecting a sync signal from the video output signal of the recording/reproducing apparatus 118; and a reproduction detector 203 for comparing the sync signal detected from the sync separator 202 with a sync signal outputted from the internal sync signal generating circuit 116. ID output portion 204 operates substantially same as the ID output operation of bit/ID output portion 110 shown in Fig. 1.

A recording/reproduction detecting method in the second embodiment of the invention will now be described.

First, a difference between the video output signals of the apparatus 118 in the reproducing mode and the recording mode will be described. Since a mechanical rotational vibration called a wow and flutter of the apparatus 118 exerts an influence on the video output signal in the reproducing mode, stability and precision of the sync signal deteriorate. Generally, since the input signal of the apparatus 118 is outputted as it is as for the video output signal in the recording

mode, it becomes the signal which coincides with the internal sync signal in the video switching apparatus 1013 and the stability as a sync signal is maintained as it is. As mentioned above, the operating mode of the apparatus 118 is detected by the difference in the video output signal of the apparatus 118.

Fig. 4 is a flowchart showing the recording/reproduction detecting method.

A horizontal sync signal is detected from the video output signal of the recording/reproducing apparatus 118 (step 1). A phase difference between the detected horizontal sync signal and an internal horizontal sync signal is detected at time T1 and time T2 (step 2) (e.g., $\theta_{\, \text{i}}, \ \theta_{\, \text{2}}$). The phase differences ($\theta_{\, \text{i}}, \ \theta_{\, \text{2}}$) θ_{2}) detected at time T1 and time T2 are compared (step 3). When the phase difference (θ) from the internal horizontal sync signal is equal to 0 or a value which is extremely close to 0 or when the phase differences compared in step 3 are substantially or nearly equal to each other $(\theta_1 = \theta_2)$, the video switching apparatus is set to the recording mode (steps 4 and 6). If there is a phase difference ($\theta \neq zero$) from the internal horizontal sync signal and when the value between the phase differences compared in step 3 are not substantially or nearly equal to each other, i.e., not $(\theta_1 = \theta_2)$, the video switching apparatus is set to the reproducing mode (steps 4 and 5).

As specific values, when a clock frequency

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(e.g., Fig. 5 INTCLK) is equal to 3.2 MHz, if the phase difference (θ) from the internal horizontal sync signal is less than 1 μ sec, then the apparatus is set to the recording mode; and, when it is equal to or larger than 1 μ sec, then the apparatus is set to the reproducing mode. When a difference of 0.3 μ sec or more occurs as a value ($|\theta_1 - \theta_2|$) between the phase differences compared in step 3, the apparatus is set to the reproducing mode.

Although the case where the number of the comparing operation times is equal to 1 has been described here, it is also possible to discriminate on the basis of data obtained by comparing more than one times in order to improve discriminating precision.

Although the horizontal sync signal is used as a signal to be compared, since it is necessary or sufficient that the phase difference can be detected, a composite sync signal (EXT SYNC) which is obtained by sync separator 202 can be also used instead of the horizontal sync signal.

A specific circuit of the reproduction detector 203 will now be described hereinbelow with reference to Fig. 5.

The reproduction detector 203 is constructed by a phase detecting portion 501 and a discriminating portion 502.

The phase detecting portion 501 comprises: counters 503 and 504; flip-flops (hereinafter, simply

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abbreviated to FFs) 505 and 506; an adder/comparator 507; and a timing generating circuit 508. The discriminating portion 502 comprises: a counter 509; a comparator 510; and an FF 511.

The timing generating circuit 508 generates timing pulses T1 and T2 from an external sync signal (EXT HD) and an internal sync signal (INT HD). Count values from the counters 503 and 504 are inputted into the FFs 505 and 506 by using the timing pulses T1 and T2 and a relative phase difference is detected by the adder/comparator 507. The counter 509 is made operative by the timing pulses generated by the circuit 508 in order to output several times in response to a respective detection result outputted from the adder/comparator 507 to the comparator 510 in order to improve precision in the reproduction detection. detection result outputs compared by the comparator 510 are substantially or nearly equal to each other, it is determined that the apparatus 118 is in the recording If they are not substantially or nearly equal, it is determined that the apparatus 118 is in the reproducing mode. This operation provides the following merit, for example. In a rare case such that (one time) detection result output at a some timing may be an erroneous result, the determination can be made by majority logic even with a few errorneous results contained in such several times outputs.

Although the discriminating method has been

described above with hardware, it can be also realized in combination with software.

When the video transmission cable 219 is not connected from the recording/reproducing apparatus 118 to the video switching apparatus 1013, since the external sync signal (EXT HD) is not detected, the FFs 505 and 506 have the same value and it is determined that the apparatus is in the recording mode. In Fig. 5, signals of INTHD, INTCLK, INTVD are generated from internal sync generating circuit 116, and signals of EXTHD, EXTSYNC are obtained from sync separator 202.

The video switching apparatus in the second embodiment of the invention as mentioned above can detect (by sync separator 202) the horizontal sync signal from the video output signal of the apparatus 118, discriminate (by reproduction detector 203) the operating mode of the apparatus 118 on the basis of a relative phase difference between the detected horizontal sync signal and the internal horizontal sync signal, and automatically switch the recording/ reproducing mode. Even if the video transmission cable 219 is not connected from the apparatus 118 to the video switching apparatus 1013, since the video switching apparatus is set in the recording mode, a mistake such that the video signal cannot be recorded can be prevented.

As described above, since the operating mode of the apparatus 118 can be detected in the video

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switching apparatus by displaying the operation state or mode of the apparatus 118 by recording/reproduction display 121 or by transmitting a detection result by recording/reproduction detection output means 123, the recording mode of the apparatus can be confirmed even at a remote place such as a monitoring room or the like. The foregoing are described exemplarily for analog video inputs signals, it appears obvious to those skilled that applications of the present inventions for digital video inputs signals are possible and covered by present claims.

As described above, according to the invention, the operating mode of the video switching apparatus can be automatically switched in accordance with the operating mode of the connected recording/ reproducing apparatus without using another control line. An erroneous operation due to a mistake of the setting, particularly, a mistake or failure in the recording can be remarkably reduced, and the operating mode of the recording/reproducing apparatus can be displayed, or a signal indicative of the operating mode can be outputted to the outside. A video switching apparatus having an advantageous effect such that the operating mode of the recording/reproducing apparatus can be confirmed even if a display screen of the recording/reproducing apparatus cannot be seen is provided.